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Remarks:

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(54) Memory card interface apparatus

(57) A removable memory card interface apparatus enables a removable memory card to communicate with a number of peripheral devices directly without the need for the memory cards host or a host computer. The Interface apparatus includes a micro-controller operating with an Internal bus to control, process and, route signals between the removable memory and a peripheral device via a series of memory and device interfaces. A control panel and/or remote control allows a user to interact with the interface apparatus. In one embodiment, the interface apparatus also allows a host computer to communicate with the memory card via one of the perripheral device posts. In other embodiments, their face apparatus serves as a memory card printer controller or as a memory card player and/or recorder when interfaced with consumer electronics.

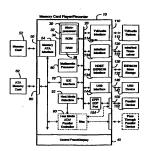


FIG. 2

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Description

FIELD OF THE INVENTION

[0001] The present invention relates to a computer inortace apparatus and in particular to such apparatus that interfaces a memory card directly with a variety of peripherals without the need for a host system or to such apparatus that interfaces a memory card with a host system via a standard port of the host system not native to the memory card.

BACKGROUND OF THE INVENTION

[0002] Solid-state memory in the form of flash memory has recently become the storage of choice in a variety of mobile & handheld devices, notably information equipment and consumer electronics products. Unlike RAM (random access memory) which is also solid-state memory, flash memory is non-volatile which retains its 20 stored data even after power is turned off. In spite of the higher cost, flash memory is increasing being use in mass storage applications where Information is stored semi-permanently in file format. Conventional mass storage, based on rotating magnetic media such as hard 25 drives and floppy disks, is unsuitable for the mobile and handheld environment. This is because disk drives tend to be bulky, are prone to mechanical failure and have high power requirements. These undesirable attributes make disk-based storage impractical in most mobile and 30 portable applications. On the other hand, flash memory is ideally suited in the mobile and handheld environment because of its small size, low power consumption, highspeed and high reliability features.

[0003] While some of the early products have been using built-in flash memory chips, current products using flash memory are mostly in the form of a removable memory card. The removability feature has the added advantage of memory-capacity fleatility. Reduced cost for the product flash, and portability and ease of information transfer from one host system to another. [0004] One common usage of removable memory

card is therefore in mobile computing such as keptop and paintop computers, PDAs (personal digital assistants). In addition to providing application programs and data storage on these mobile hosts, the removable memory card provides a convenient way to exchange data and files between different hosts. For example, by moving the memory card from a mobile host to a desktop host, data in the field can be easily synchronized with that on the desktop. In the case of a limited-restured mobile host, the transferred data can be better manipulated on a full-featured desktop computer.

[0005] A corrunon interface has been established for memory cards. It is known as the PC card standard or PCMCIA Interface as specified by the Personal Computer Memory Card Interface Association. The standard establishes both the mechanical and electrical characteristics of the memory card interface. It is designed specifically for solid-state memory application although the interface has, since its inception a few years ago, been adapted to become a general purpose interface for cardbased devices such as moderns, network adapters and even hard drives. It has a size about that of a credit card and comes in Type I, II and III with increasing thickness from a couple of millimeters to several millimeters. There is a 68-pin socket on one card edge for mating with a connector located on a PC card slot of a host device. While PC card slots have become standard equipment on laptop and notebook computers, they have hardly been deployed on desktop personal computers (PCS). Thus, for a desktop computer to communicate directly with a PC card, a PC card slot must first be installed.

[0006] Currently, there are two main flash memory architectures that conform to the PC card standard, namety ATA flash and Linear flash.

- (DOOT) ATA flash, with its 512 bytes flash sector architecture, mimics the DOS file structure of an ATA (Astended Technology Attachment, i.e., one that communicates with a standard PC ISA bus) hard drive. In addition, the memory operation is under the control of a dedicated intelligent memory controller which also harmony expensions are not one of the control of a dedicated intelligent memory controller which also harmony expensions are not of the control of a dedicated intelligent memory operations. To a PC running DOS and MS Windows(fi) operating system, a card with an ATA controller appears like another expensions of the control of the cont
- relize on the intelligence and processing power of the host computer to handle the memory operations. It stores and communicates data in large blocks each having a size much larger than the DOS disk sector of 512 bytes. Its address space can be compatible with that of the system memory and could be seen as an extension of system RAM or ROM. Thus, it is possible to execute application programs directly from it rather than having to first load the application program into the system memory. When used as mass storage, Linear flash re-
- quires a software layer to translate data in blocks form to the file format of the operating system and vice versa. This software layer is called Flash File System (FFS). An alternative software layer is called Flash Translation Layer (FTL).
- [0009] Thus, when plugged into a host PC system, some PC memory cards are immediately recognized as another disk drive while others require addition software and/or hardware to operate.

[0010] Removable memory card is also being incorporated into a new class of "smart" consumer electronics products. With products like smart cellular phones and pagers, the memory card is used to store data such as address book and personal information, or to record voice, e-mail, faxes, and pager messages, or to cache stock quotes and news feed. Again, the removable memory cards allow data to be portable between similar host devices and for the field data to be exchanged with that of the desktop computers.

[0011] With smaller handheld devices, the need arises for even smaller removable memory cards. A number of smaller form factor card standards have emerged. Three competing standards, namely CompactFlash, Miniature Card and SSFDC are being supported respectively by the CompactFlash Association (CFA), the Solid State Floppy Disk Card Group (SSFDC) and the Miniature Card Implementers Forum (MCIF). More recently, yet another card standard known as MultiMedia card (MMC) is being announced. All these cards are about postage stamp size with different electrical interfaces and slight difference in dimensions. Unlike one main PC card standard, there are then several smaller form factor card standards each specifying somewhat difference mechanical and electrical Interfaces. A device or product will need a particular type of card slot for 20 a particular type of smaller form factor card. However, most of the smaller form factor cards are provided with an adapter to conform to the PC card standard. Typically, the adapter is in the form of a Type II PC card for embedding the smaller form factor card Into. In this way, 25 they may be used in an existing PC card (PCMCIA) slot, possibly with addition software and hardware as reauired.

[0012] Removable memory card is rapidly entering the consumer marketplace because of widespread 30 adoption by consumer electronics products such as digital cameras and digital audio recorders. The memory card replaces conventional photographic film or magnetic tape and provides virtually an unlimited amount of renewable recording media. For example, the memory 35 card records image frames captured by a digital camera In the form of a compressed image file. A 4MB flash memory card may hold approximately 40 frames (VGA resolution 640 by 480 pixels) at the rate of 100KB per compressed file. When a card is filled up, a new or erased one could quickly replace it. The added advantage over conventional film is that the memory card could be erased and reused again and again when the existing data on it is no longer needed or has been archived to another storage.

[0013] Once the image files have been stored on the memory card, they can hypically be called up frame by frame and displayed on a built-in small LCD screen provided on the camera. When hard copies of the recorded images are to be made, the camera sends its output to a specialized photo printer dedicated for this purpose. More common soutlines have been to move the memory card into a desktop or a laptop hest computer equipped with a slot for receiving such memory card. The image files can then be displayed on the computer with or without further manipulation before being printed on an attached standard printer.

[0014] Currently, a smaller form factor memory card

- may have a capacity as high as 40MB and this capacity will be likely doubled with every new generation of flash memory devices. With this amount of capacity, it is a viable storage for multimedia information. Consumer
- 5 products such as digital voice recorders and digital audio recorders will be able to record voice and audio information onto arremovable memory card. Similar to the digital camera case, the recorded audio information on a memory card can be played back on its original host or on a desktop or latopto host.
- [0015] Thus in most cases, the memory card is intended to be used in conjunction with its original host device or with a PC host that is equipped with a compatible card sto for receiving the card. However, because many of
- slot for receiving the card. However, because many of the applications have wide consumer appeal, a will be desirable to process, render and playback the Information recorded on a memory card in a consumer environment instead of the original host or a PC host. Also, it will be desirable to record audio, video, and images from
- will be desirable to record audio, video, and images from consumer electronics machines or from settop boxes for internet access or from "thin" "network computers" (NC).

OBJECTS AND SUMMARY OF THE INVENTION

- [0016] It is therefore a general object of the invention to provide an apparatus that interfaces a removable memory card directly with a variety of rendering devices without the need for the memory card's native host device nor a host personal computer.
- [0017] It is another general object of the invention to provide an apparatus that interfaces a removable memory card with a standard port of a host computer not native to the removable memory card.
- 5 [0018] It is an object of the invention to provide a low-cost, easy-to-use memory card player that plays directly into existing consumer electronics such as television and audio equipment.
- [0019] It is another object of the invention to provide a lowcost, easy-to-use memory card recorder that records audio and video Information directly from existing consumer electronics such as television and audio equipment.
- [0020] It is another object of the invention to provide a lowcost, easy-to-use memory card recorder that records multimedia information directly from settop boxes for internet access and stripped-down "network computers."
- [0021] It is another object of the invention to provide an apparatus that Interfaces a removable memory card directly with a standard printer. [0022] These and other objects of the invention are
- accomplished briefly by having an interface apparatus is adapted to communicate with a removable memtor on one hand and a plurafity of peripheral devices on the other hand. The interface apparatus includes an cro-controller operating with an internal bus and includes one or more memory interface modules for trans-

lating a memory card's signals into that compatible with an internal bus. The interface appearatus also includes one or more device-specific interface modules for translating the device signals into corresponding internal bus compatible signals. In this way, the micro-controller is able to control and route any sets of signals on the Internal bus between the memory card and a device in spile of the fact they employ different information formats and protocom.

[0023] According to one aspect of the invention, the interface apparatus includes a control panel and display for user interaction. In one embodiment, a remote control through an infrared port is provided as a remote extension of the control panel. Thus, the interface is easy to operate, much like that of the familiar consumer electronics.

[0024] According to another aspect of the Invention, the interface appearatus supports a host mode operation in which one of the peripheral ports, such as the parallel port, is connectable to a host computer. In this way communication can be established between a memory card and the host computer even though the two do not have an interface in common.

[0025] The invention provides a flexible framework for implementing a removable memory card interface that 2s is able to communicate with a veniety of peripheral devices and even host computer. In its simplest embodiment, it can be a very low cost, declared player of digital images or audio files on consumer audio/video electronics. Alternatively, it can be a very low cost memory card interface for printing digital images directly to a standard printer. In a multi-feature embodiment, it can be a general purpose, multi-system memory card interface.

BRIEF DESCRIPTION OF THE DRAWINGS

[0026] The Invention will now be described by way of example with reference to the drawings in which:

Figures 1A-1C illustrates the typical environment in which a removable memory card is used; Figure 2 is a schematic block diagram of the interface apparatus according to a general embodiment

of the invention;
Figure 3 is a schematic block diagram of the interface apparatus when operating in "Host Mode," in which a memory card is interfaced with a host computer through a parallel port thereof, according to a preferred embodiment of the invention; and Figure 4 is a flow diagram illustrating the operation of the interface apparatus shown in Figures 2 and 3, according to a preferred embodiment of the invention.

DETAILED DESCRIPTION-OF THE PREFERRED EMBODIMENT

[0027] Figures 1A-1C illustrates the typical applica-

memory card is used. In particular, Figure 1 A illustrates a removable memory card being used with a digital still camera. The connection is by means of either a PCM-CIA slot or one of the smaller form-factor card interfaces. Figure 18 illustrates a removable memory card being used with a digital audio recorder. The audio recorder abe a voice recorder such as a dictation meachine, or it can assume a form suitable for recording high-fidelity music. Figure 1 C illustrates a removable memory card being used with a leptop computer or a desktop personal computer by connection through a PC card (PCMCIA) Interface installed thereon.

[0028] Figure 2 is a schematic block diagram of the

- s interface apparatus according to a general embodiment of the invention. Essentially, an interface apparatus 10 comprises a housing and mounted thereon is a set of memory card connectors for connection to one or more removable memory cards, and also a set of peripheral connectors for commercion to one or more peripheral devices. An overall architecture is to convert the date format and protocol (signales) of each type of memories supported by the interface apparatus into a set of uniform signales on a backplane by means of an appropriate or memory interface. Similarly, the signals of each peripheral device supported by the interface apparatus are converted by an appropriate peripheral interface into the seme common set of uniform signals or the backplane. In this way, a micro-controller 30 earn to more processes and route any sets or signals on the internal buz 200 can the backplane.
- in this way, a micro-controller 30 can control, process or and route any sets of signats or an internal bus 20 (or the backplane) even though the source and destination devices "speak" different signals. Thus, a memory card and a pertpheral device are coupled together via a series of-device interfaces communicating with each other strough an internal bus 20.
- [0029] The micro-controller 30 comprises a microprocessor 32, PCM 34 and RAM 38 all of which are in communication with the internal bus 20. In typical oppilcations and configurations, a low cost microprocessor or will suffice to handle data routing type of functionality. A more powerful microprocessor may be employed if the application calls for a substantial amount of data or signal processing, such as compression or decompression or rendering complex display or printer files. The microsortioler operates under a microcode or BIOS stoned in FOM 34. The microcode can be updated via one of the memory ports or peripheral ports. Data transferred between a memory device and a peripheral device is
- [0030] A user interface is provided by a control panel display 40 on the housing of the interface apparatus 10. The control panel/display is in communication with the internal bus 20 via an I/O interface, frost shown). In the preferred embodiment, the control panel/display comprises buttons and LEDS and display similar to that found in consumer electronics. For example, some of the buttons may be similar to that of a VCR or a CD player which are useful for a user to navisate with when

buffered and cached in RAM 38.

viewing images or listening to audio on a TVAudio system. In one embodiment, depending on the commanda button pressed, the micro-controller transfers databommands to or from a memory card. The delsylen ymay have LEDS to display the status of the operation such as "Command Completed Successfully" or "Error Flead the memory card" or "Peripheral Device Not Ready" or "Media full," etc. Alternatively, a LCD display may provide even more flexibility.

[0031] In one embodiment, the set of memory card 10 connectors includes a connector 50 that conforms to the PCMCIA interface for receiving a Non-ATA flash PC card 52. A memory/ATA interface 54 is employed to convert the signals to be ATA compatible. As described in an earlier section, this is accomplished either by an additional software layer or by operating the memory in conjunction with a memory controller provided by the memory manufacturer as an embedded chipset. If the Implementation is with an edditional software layer, the software drivers can either reside in a memory of the Memory/ATA Interface 54 or in ROM 34 or loaded into RAM 36. Once the signals are ATA compatible, they are coupled to the internal bus 20 via an IDE interface 70. [0032] For those memory cards whose address space is an extension of the system memory space of the interface apparatus, they can be used to augment the ROM 34 or RAM 36. In some applications, an application program or applet may be supplied with or without the data on one of these removable memory cards and executed-in-place.

[0033] In one embodiment, the set of memory card connectors includes at least two memory connectors. One is connected to a first memory eard with recorded data, and the other is connected to a second memory card with an application program. In this way, the application program on the second memory card can be executed-in-place to process the data from the first card. [0034] In another embodiment, the set of memory card connectors includes a connector 60 that conforms to the PCMCIA interface to receiving an ATA flash PC card 62. In this case, since the flash memory is already ATA compatible, no additional processing need be done before coupling the signals to the internal bus 20 via an IDE interface.

[0035] In the case of smaller form factor flash memory cards, they can be connected to the appropriate PCM-CIA connectors 50, 60 via an adapter as mentioned before.

[0036] In another embodiment, the same PCMCIA interface is provided to accept both ATA and non-ATA 50 flash memory. Since media identification is incorporated into flash memory cards, internal logic (not explicitly shown) responsive to the sensed media type will process the signals accordingly by routing or not routing through the Memory/ATA interface 54.

[0037] On the peripheral end of the Interface apparatus 10, the set of peripheral connectors depends on the specific need and application.

- [0038] In one embodiment, the set of peripheral connectors includes a connector 110 that conforms to the video and audio input and output of a television and audio system 112. Typically, the graphics data on the mem-
- 5 ory card is in the form of standard compressed image files, such as JPEG files. Similarly in audio applications, the audio data is stored as well-defined compressed sound files. A multimedia processor 80 in communication with the internal bus 20 is employed to compress or
- 10 decompress the various multimedia files. In the case of playing back a recorded multimedia file from a memory card, the compressed multimedia file is decompressed by the multimedia processor 80 before being converted by a TV/Audio interface 114 to standard TV and audio
 - by a Trytodio interlease. This to standard it was a displayed on the TV screen frame-by-frame by means of a user operating the control panel 40 or a remote control to be described later.
- [0039] In the case of recording multimedia Information from the TVALudio system, the signals from the TVALudio system, the signals from the TVALudio system are compressed by the multimedia processor 80 before being written to a memory card. The multimedia processor 80 as well as the TVALudio Interface 114 are readily implemented by commercially available set only display the total system of the total sy

[0040] Alternatively, the compression or decompression of the multimedia data may be accomplished by software in RAM 36 and executed by the microprocessor 32

- [0041] In another embodiment, the set of peripheral connectors includes a connector 120 that conforms to an infrared port which is connectable to an infrared transceiver 122. An infrared interface 124 converts the internal bus signals into that of the Infrared transceiver.
- The infrared port may be used to communicate through the infrared transceiver 52 with other infrared enabled peripherals, such as printers and even host computers.

 [0042] In a preferred embodiment, the infrared port or a similar additional one is used to implement a remote control with is an extension of the control panel 40.
- [0043] In another embodiment, the set of peripheral connectors includes a connector 130 that conforms to an IDE (Integrated Device Electronics) port which is a connectable to similar type of mass storage 125 such as a hard disk or a CD-ROM or a tape drive. An interface 134 serves as an IDE interface that translates signals between the internal bus 20 and the mass storage 132. Alternatives the connector 60 may conform to a SCSI.
- Alternatively, the connector 60 may conform to a SCSI 60 (Small Computer System Interface) port connectable to SCSI peripheral devices, in which case the interface 134 is a SCSI interface. The mass storage is useful in backing up the data from the memory card.
- [0044] In another embodiment, the set of peripheral connectors includes a connector 140 that conforms to a USB (Universal Serial Bus) port which is connectable to a USB peripheral device 142. A USB interface 144 translates signals between the internal bus 20 and the

USB peripheral device 142.

[0045] In another embodiment, the set of peripheral connectors includes a connector 150 that conforms to a PC parallel port which is connectable to a parallel peripheral 152 such as a printer or a mass storage using a parallel interface. A parallel Interface 144 translates signals between the internal bus 20 and the parallel peripheral device 142.

[0046] In one application, the parallel peripheral device is a printer. Image files stored on a memory card are rendered by the multimedia processor 80 before being printed by the printer.

HOST MODE

[0047] While the interface apparatus is advantageously used to connect a memory card to a variety of peripheral device, it is also possible to connect a memony card to a host computer through the parallel connector 150. This allows a memory card to communicate with 20 a host computer even when a PC card slot is absent. [0048] Figure 3 is a schematic block diagram of the Interface apparatus 10 when operating in "Host Mode". In a preferred embodiment, the interface apparatus 10 Includes a host mode, ATA/Parallel Controller 90. The 25 dedicated controller essentially translates signals between ATA format and the parallel format. In this implementation, the memory card signals are translated into ATA compatible signals which in turn are translated to parallel signals at the connector 150 to which a host 30 computer 156 is connected. A host mode select logic 92 in communication with the internal bus 20 is used to enable or disable the ATA/Parallel Controller 90. During operation, a user selects the host mode through the control panel 40. In response, the micro-controller 30 activates the select logic 92 which in term activates the ATA/ Parallel Controller 90.

[0049] In the preferred embodiment, the set of peripheral connectors includes a connector 160 that conforms to another parallel port. This parallel connector 160 is to 40 be used as a pass-through parallel port for connection to a pass-through parallel peripheral device 162. Typically, such a pass-through port is used to connect to a peripheral 162 that is normally connected to the nost computer 156. However, since the host parallel port is already used up in connecting to the interface apparatus, the pass-through port provides a substitute parallel connection to the peripheral 162.

[0050] The ATA/Parallel Controller 90 with passthrough port support may be implemented by commer- 50 cially available ASIC chips such as those available from Shuttle Technology Inc., Fremont, California, U.S.A. [0051] Figure 4 is a flow diagram illustrating the operation of the interface apparatus shown in Figures 2 and according to a preferred embodiment of the invention. Essentially, based on the user Input settings, the sensed media and connection, and predefined logic, the Interface apparatus sets up the appropriate interface mod-

- ules, hardware and/or software to effect the Interfacing
- [0052] The operations may be grouped into an Initialization of the overall system, an initialization of the memory card port(s), and an initialization of the device ports, according to the following steps.
- [0053] Step 200: system Initialization begins.
- 100541 Step 202: a self test is performed and the system BIOS and/or operating system takes control of the Microcontroller.
- [0055] Step 204: the user-input settings are constantly monitored and responded to. Control proceeds to Step 210.
- 100561 Step 210: memory card port(s) initialization begins.
 - [0057] Step 212: the memory card ports are sensed to detect the presence of a card.
 - [0058] Step 214: if no memory card is found, a status message. "No Memory Card Installed" is output. The system then goes into a recovery mode or abort mode depending if the error is rectified.
 - [0059] Step 216: if a memory card is found, its type is determined and its system information is read.
 - [0060] Step 218: the micro-controller sets up the memory Interface module appropriate to the memory type.
- 100611 Step 219: the memory card interface portion of the interface apparatus is now ready. Control proceeds to Step 220.
- [0062] Step 220: device ports initialization begins [0063] Step 222: the Host mode choice is read from the user-input settings.
- [0064] Step 224: if Host mode is not selected, then Device mode is, and control proceeds to Step 240. If Host mode is selected, control proceeds to Step 230.
- [0065] Step 230: host mode initialization begins. [0066] Step 232: the designated parallel port is checked for a host computer connection.
- [0067] Step 234: if not host computer connection is found, a status message, "Host not connected" is output. The system then goes into a recovery mode or abort mode depending if the error is rectified. [0069] Step 236: if a host computer is found in proper
- condition, the parallel interface module is activated. [0069] Step 238: the parallel port is now ready for Host
 - mode transfer. Control proceeds to Step 250 [0070] Step 240: Device mode initialization begins.
 - [0071] Step 242: the user-designated device port is sensed for the presence of a device connection.
 - 100721 Step 244; if no device connection is found, a status message, "No Device Connected" Is output. The system then goes into a recovery mode or abort mode depending on whether the error is rectified. [0073] Step 246: if a device connection is present, its
 - characteristics are sensed. [0074] Step 248: the device Interface module associ-
 - ated with the port is activated.

[0075] Step 249: the device is now ready for commu-

25

nication. Control proceeds to Step 250.

[0076] Step 250: the interface apparatus is now ready

[0076] Step 250: the interface apparatus is now ready for operation.

[0077] It will be appreciated from the above, therefore, a low cost, standalone memory card readerfecorder 5 that is simple to use and flexible has been described. No host device, such as the memory card's nather host, nor a general purpose personal computer, is required. Thus, the invention provides a particularly simple and effective solution to the problems stated in the introduc-

[0078] While the embodiments of the various aspects of the present invention that have been described are the preterred impermentation, those skilled in the art will understand that variations thereof may also be possible. In For example, while the host mode has been described in the preterred embodiment to employ a parallel port, other device ports such as the intered port or the USB port are equally feasible. Similarly, while the memory ports have been described with respect to a FCMCIA anterface, it is apparent that other established smaller form lactor card inferfaces are also possible. Therefore, the invention is entitled to protection within the full scope of the appended claims.

Claims

A memory card interface apparatus comprising:

an internal bus;

memory card;

an external memory connector for removably

coupling to a memory card; a memory port connected to said external memory connector and coupled to said Internal 35

bus; a plurality of external device connectors, each for coupling to a device that operates with said

a device port connected to each external memory connector and coupled to said internal bus; a control panel coupled to said internal bus for specifying an operation mode of the interface apparatus:

a Interface controller coupled to said Internal
bus and responsive to said control panel for enabling communication between said memory
card and one or more devices coupled to said
plurality of external device connectors.

 A memory card-interface apparatus as in 1, further comprising a parallel port coupled to said internal bus and to one of said plurally of external connectors, thereby supporting communication through a parallel port connection between said memory card and a peripheral device.

3. A memory card interface apparatus as in 1, further

comprising a Universal Serial Bus interface coupled to said internal bus and to one of said plurality of external connectors, thereby supporting communication through an Universal Serial Bus connection between said memory card and a peripheral device.

4. A memory card interface apparatus as in 1, turber comprising a mass storage to parallel interface coupled to said internal bus and to one of said plurally of external connectors, thereby supporting communication through a mass storage to parallel connection between said memory card and a mass storage device.

 Amemory card Interface apparatus as in 4, wherein said mass storage to parallel interface includes an ATAPI Interface.

 A memory card Interface apparatus as in 4, wherein said mass storage to parallel interface includes a SCSI interface.

 A memory card interface apparatus as in 1, further comprising a television signal converter coupled to said internal bus and to one of said plurality of external connectors, thereby supporting communication through a parallel port connection between said memory card and a television.

30 8. A memory card interface apparatus as in 1, further comprising a sound signal converter coupled to said internal bus and to one of said plurality of external connectors, thereby supporting communication through a parallel port connection between said memory card and an audio device.

 A memory card interface apparatus as In 1, further comprising a memory to ATAPI converter coupled to said internal bus, thereby supporting communication between said memory card and said interface controller through a ATAPI interface.

 A memory card Interface apparatus as in 1, wherein said device coupled to one of said plurality of external connectors includes a host computer, and said one of said plurality of external connectors includes a parallel interface.

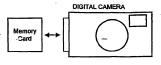


FIG. 1A

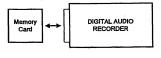


FIG. 1B

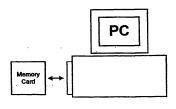


FIG. 1C

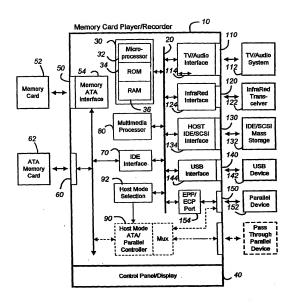


FIG. 2

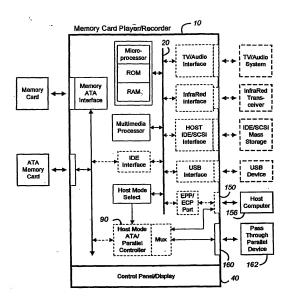


FIG. 3

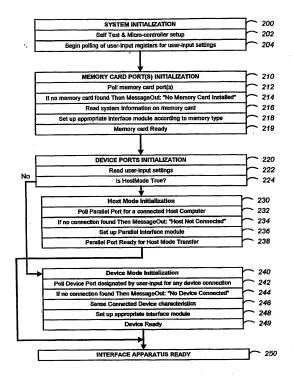


FIG. 4